H.264 I & P Frame Encoder & Decoder

PRODUCT DESCRIPTION


The encoder takes in uncompressed video input and encodes as Elementary Stream (ES) or optionally as Transport Stream (TS). The decoder decodes the ES or optional TS stream and the decompressed images are stored for display. The codec requires external DDR memory for storing the input video data to the encoder and the decoded video output for display.

The codec solution is available in both FPGA netlist and source code licensing models. CoreEL can also customize the core according to end application/customer requirements.

Typical Application include:
- Broadcast video
- Wireless microwave transmitters and receivers
- Video surveillance & law Enforcement
- Medical
- Aerospace & defense

KEY FEATURES

<table>
<thead>
<tr>
<th>Device</th>
<th>Max CABAC Bitrate</th>
<th>Max CAVL C Bitrate</th>
<th>HD (1080p) Frame-rate</th>
</tr>
</thead>
<tbody>
<tr>
<td>Kintex-7 / Virtex-6</td>
<td>100 Mbps</td>
<td>600 Mbps</td>
<td>60 fps</td>
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<tr>
<td>Virtex-5</td>
<td>50 Mbps</td>
<td>300 Mbps</td>
<td>30 fps</td>
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Note: This IP core can be made available on other Xilinx-7 series family FPGA devices too.

- Supports High profile coding
- Bit depth upto 10 bits
- Monochrome, 4:2:0 and 4:2:2 chroma format support
- Supports resolutions/frame-rate up to 1920x1080p30/60
- H.264 I & P frame codec has been validated in hardware using
  - Video Quality Experts Group (VQEG) sequences
  - European Broadcasting Union (EBU) sequences
  - Sequences from Blender Institute, Netherlands
- Supports progressive and interlaced formats
- Rate distortion optimization
- CBR and VBR rate control
- Very low latency solution
- CAVLC & CABAC entropy coding

**PART NUMBER(S)**

Following variants of this IP core are available

<table>
<thead>
<tr>
<th>Part Numbers</th>
<th>Variants</th>
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</thead>
<tbody>
<tr>
<td>CI1040</td>
<td>H.264 I &amp; P Frame Encoder &amp; Decoder</td>
</tr>
<tr>
<td>CI1011</td>
<td>Hi10 Intra Profile (Also compliant with SMPTE RP2027 standard for AVC-Intra Class 50)</td>
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<tr>
<td>CI1012</td>
<td>Hi422 Intra Profile (Also compliant with SMPTE RP2027 standard for AVC-Intra Class 100)</td>
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<tr>
<td>CI1032</td>
<td>Standalone Entropy Encoder and Decoder</td>
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